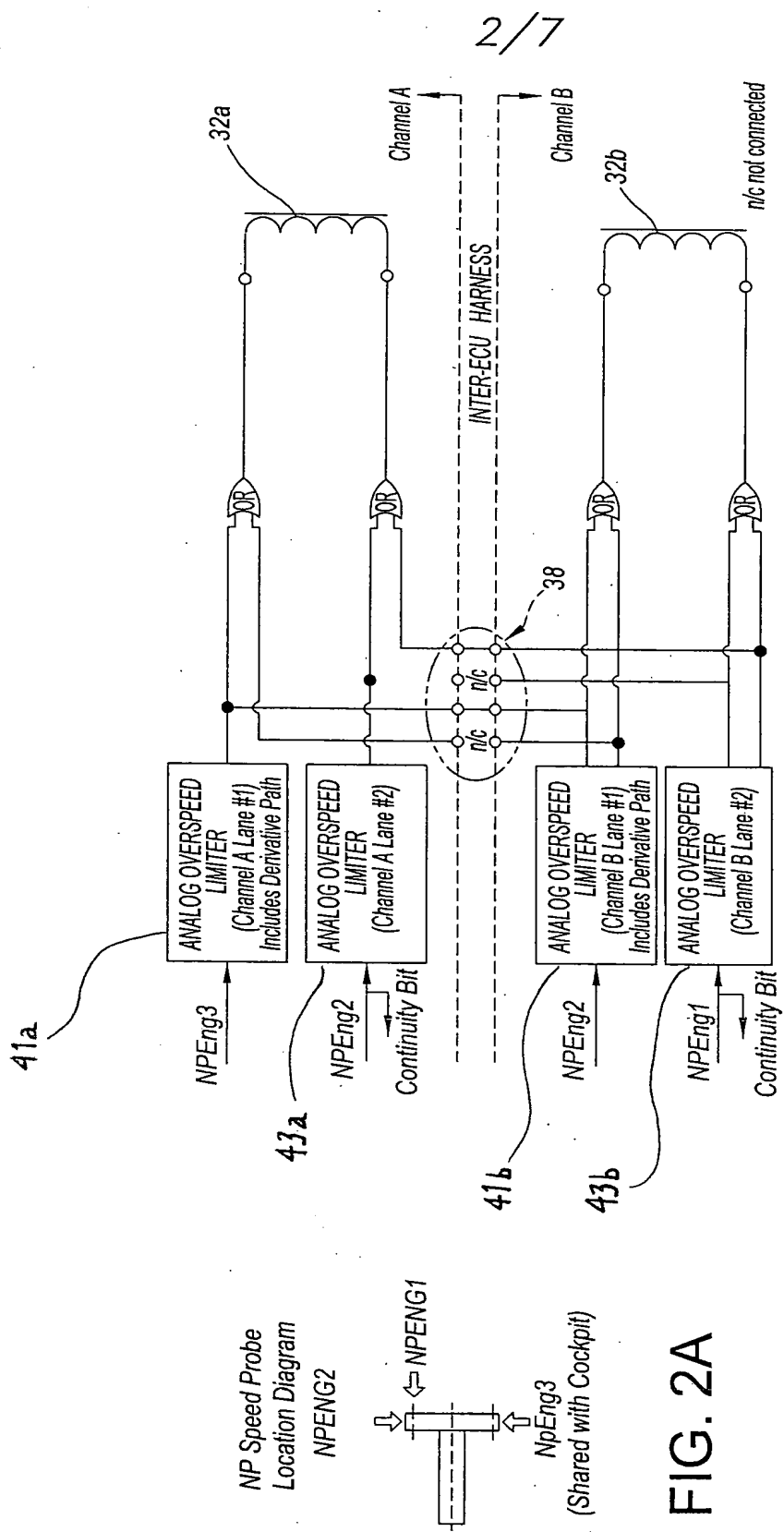


FIG. 1

+



+

FIG. 2

FIG. 2A

# SHUTDOWN ONLY SYSTEM WITH HARDWARE LATCH (CHANNEL A)

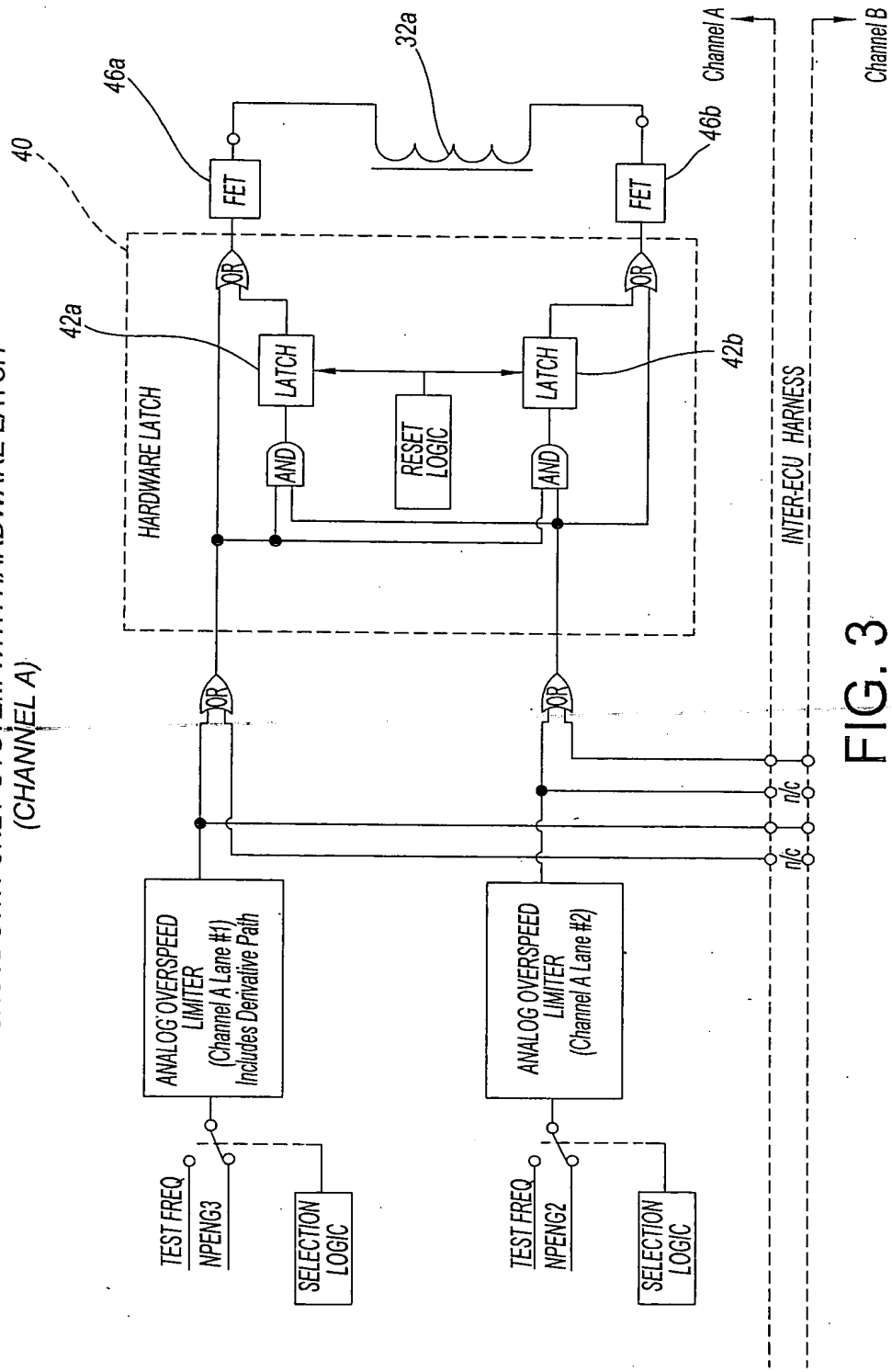


FIG. 3

# SHUTDOWN ONLY SYSTEM HARDWARE LATCH & SW TEST INTERFACES

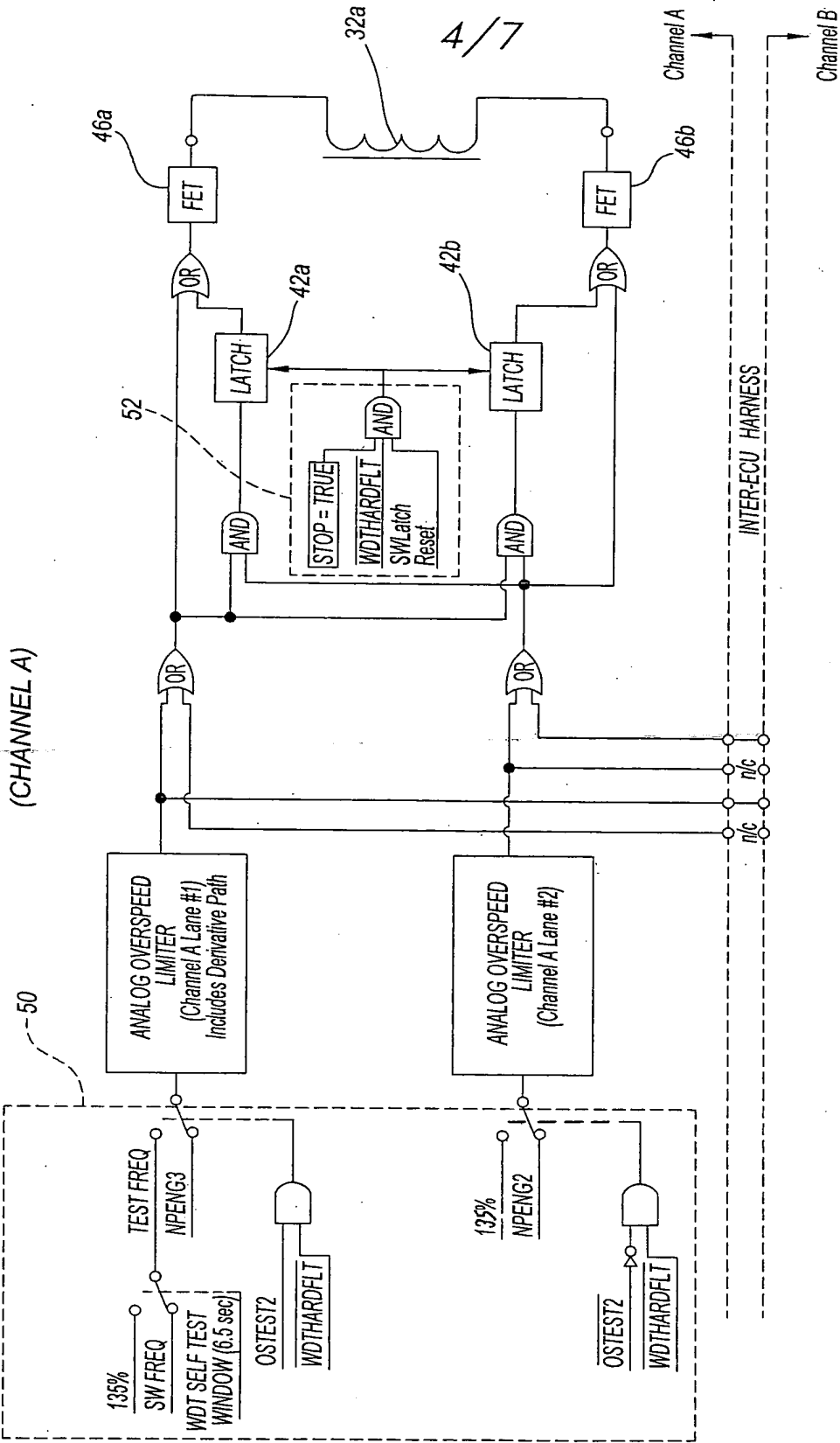
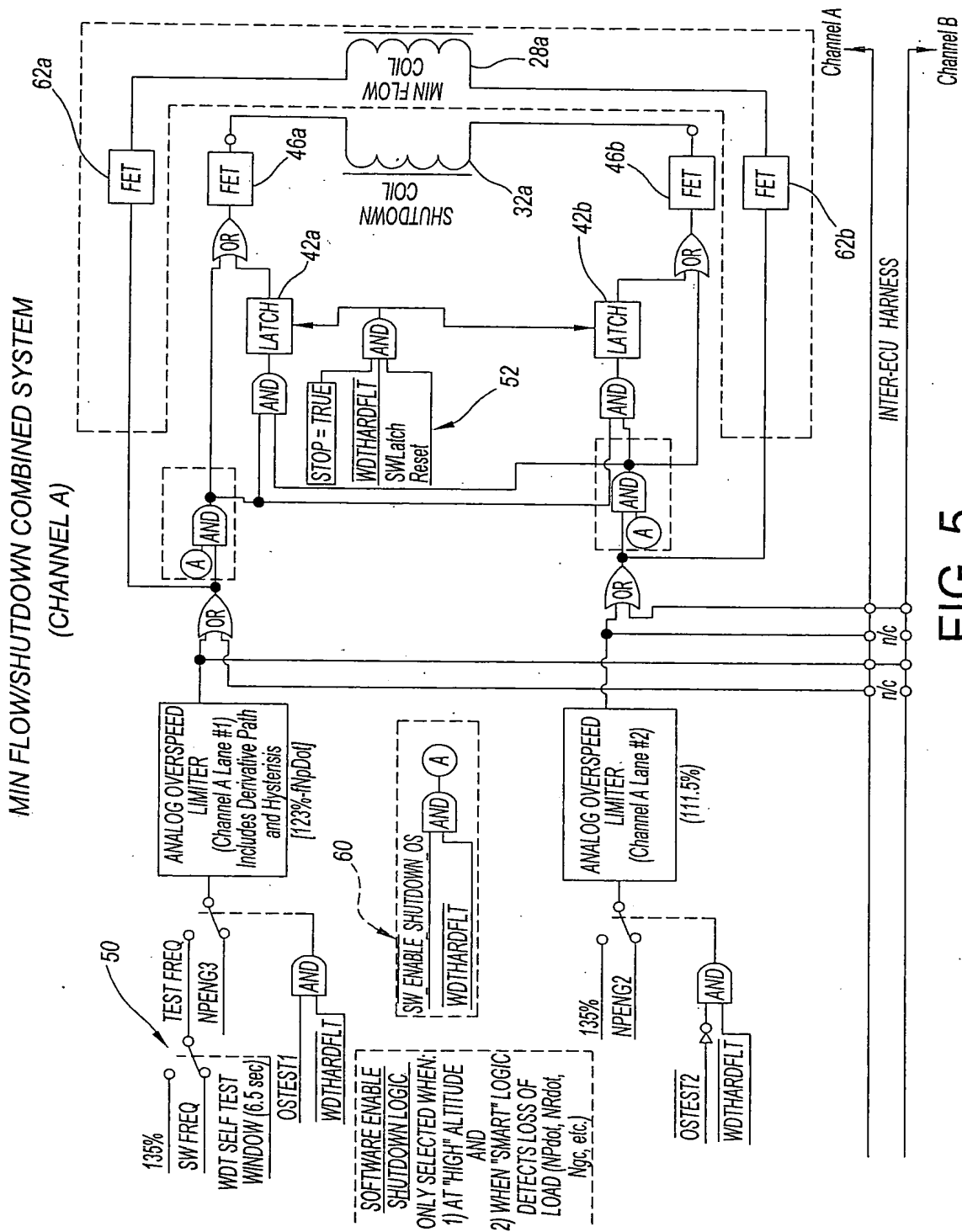


FIG. 4



# MIN FLOW/SHUTDOWN COMBINED SYSTEM WITH CROSS-ENGINE INHIBIT

(CHANNEL A)

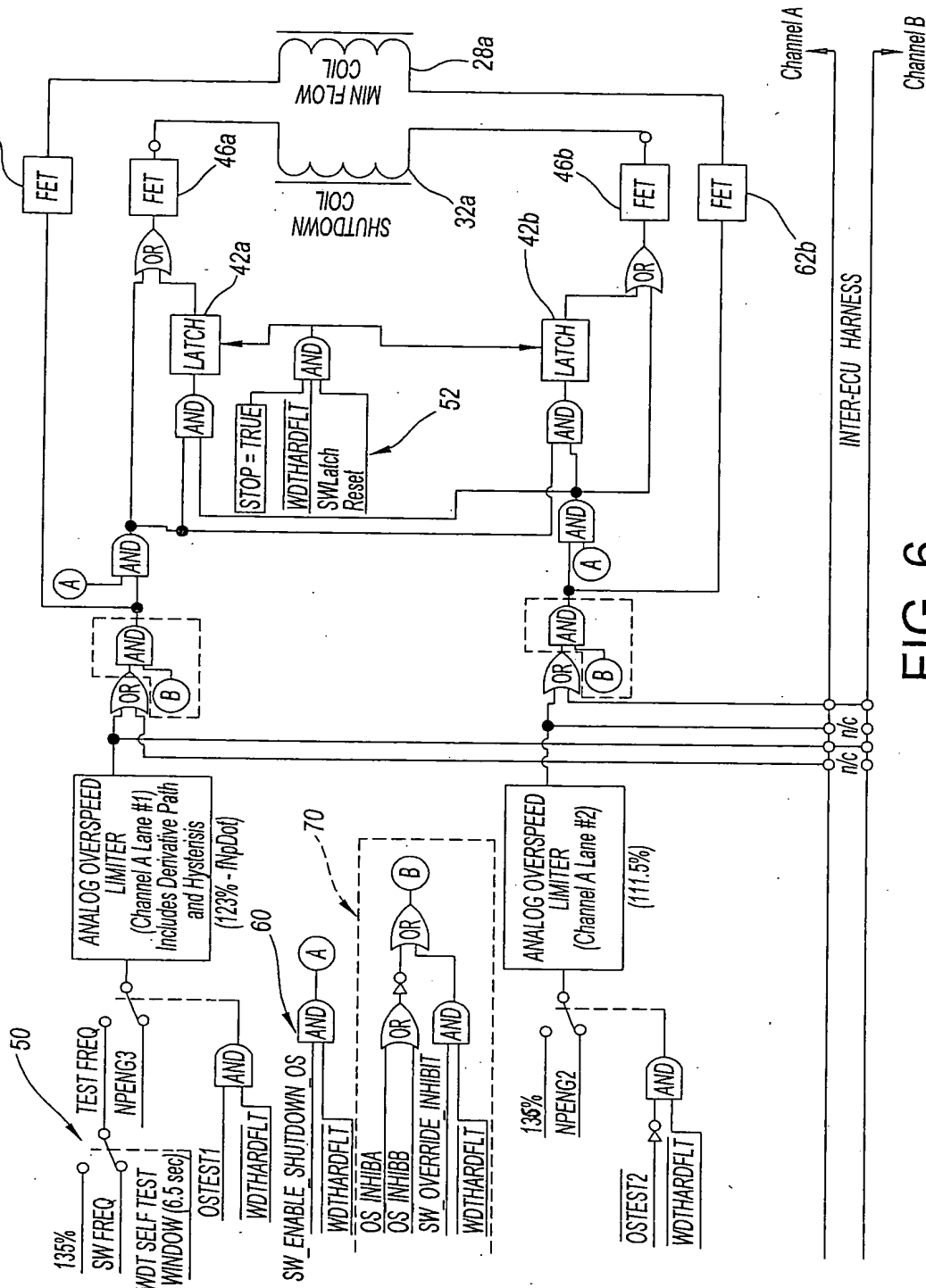


FIG. 6

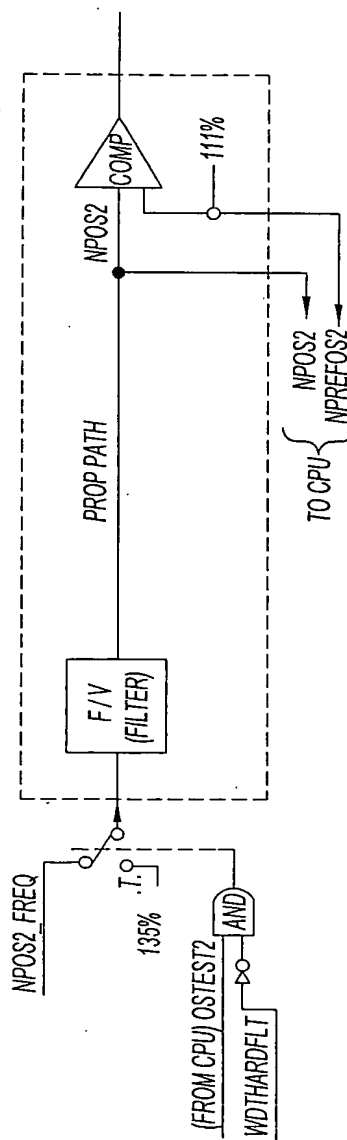
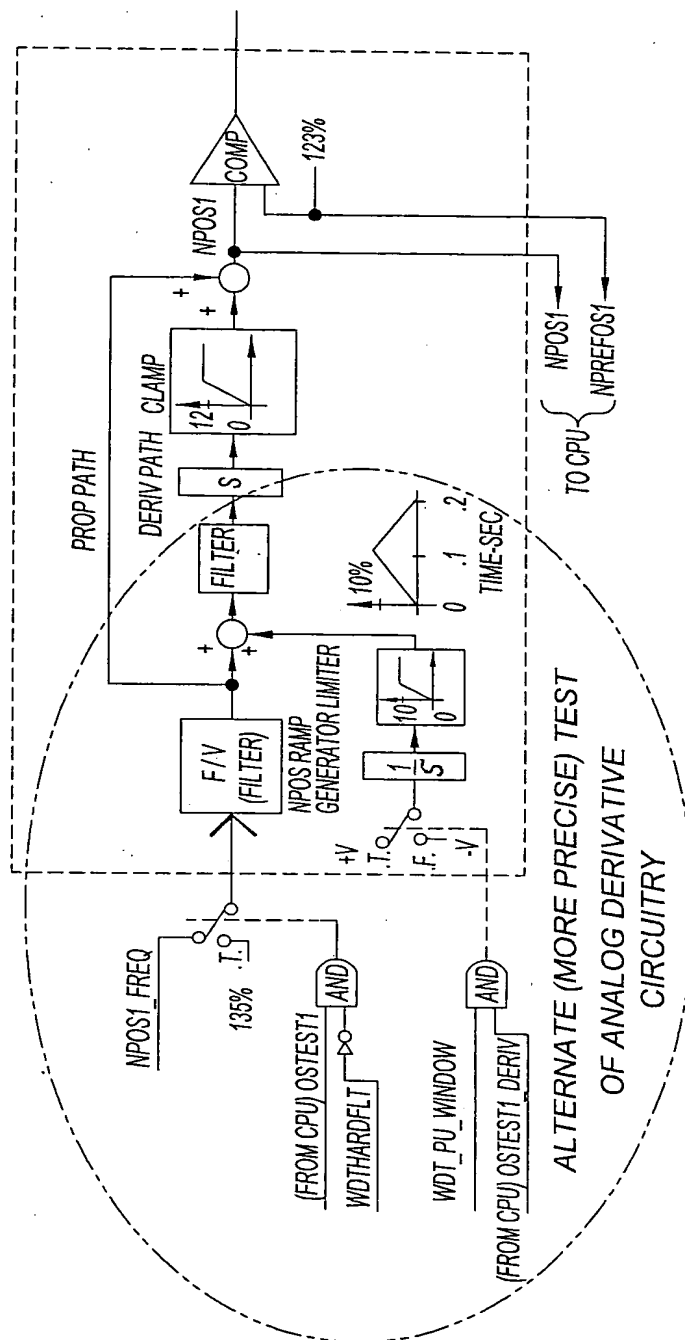


FIG. 7